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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of

Nair, et al.

Serial No.: Not Yet Assigned

Group Art Unit: Not Yet Assigned

Filing Date: Concurrently Herewith

Examiner: Unknown

For: METHOD AND APPARATUS FOR FAULT-TOLERANCE VIA DUAL THREAD
CROSSCHECKING

Assistant Commissioner of Patents
Washington, D.C. 20231

11017 U.S. PTO
10/083579
02/27/02

INFORMATION DISCLOSURE STATEMENT

Sir:

Under the provisions of 37 CFR §1.97 through §1.99 and pursuant to applicant's duty of disclosure under 37 CFR §1.56, applicant respectfully brings the following document listed on the attached form PTO-1449, to the attention of the Examiner in charge of the above-identified application. A copy of the listed document is provided herewith for the convenience of the Examiner.

This citation does not constitute an admission that the reference is relevant or material to the claims. It is only cited as constituting related art of which the applicant is aware.

It is respectfully requested that the listed reference be considered by the Examiner and formally made of record in this application.

Please charge any deficiencies in fees and credit any overpayment of fees to Assignee's Deposit Account No. 50-0510.

Respectfully submitted,



Frederick E. Cooperrider
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Date: 2/27/02
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(Use several sheets if necessary)

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DATE	
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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

		Steven K. Reinhardt and Shubendu S. Mukhrjee, "Transient Fault Detection via Simultaneous Multithreading," Paper appearing in 27th Annual International Symposium on Computer Architecture, June 2000, 12 pages

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.